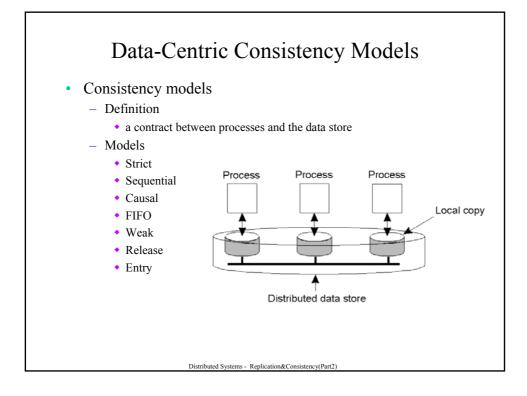
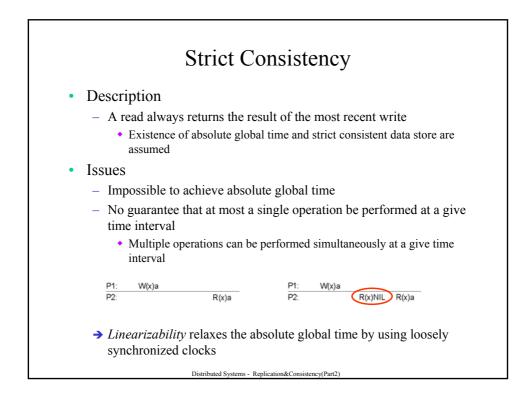
## Distributed Systems (ICE 601) Replication & Consistency - Part 2

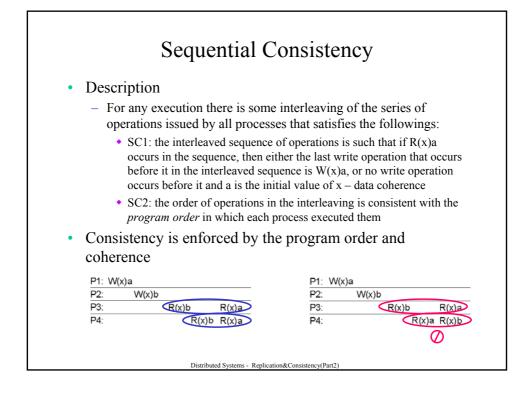
Dongman Lee ICU



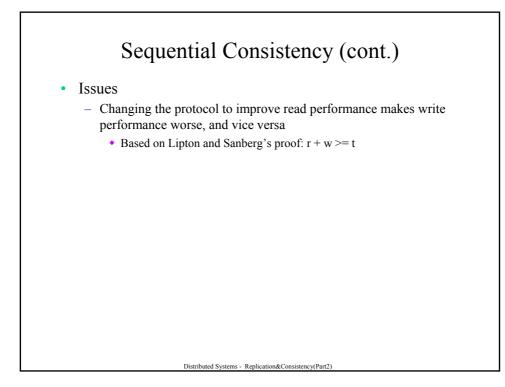
- Introduction
- Replication Model
- Request Ordering
- Consistency Models
- Consistency Protocols
- Case study
  - Transactions with Replicated Data
  - Lazy replication
  - ISIS

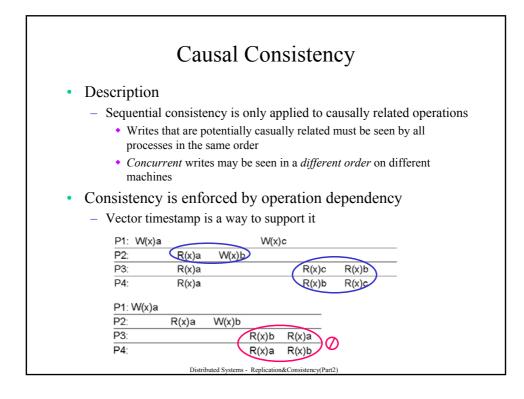


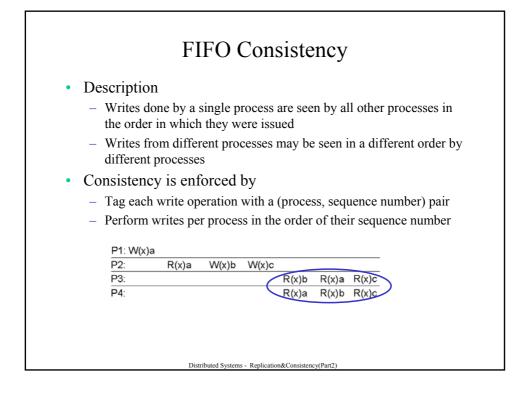


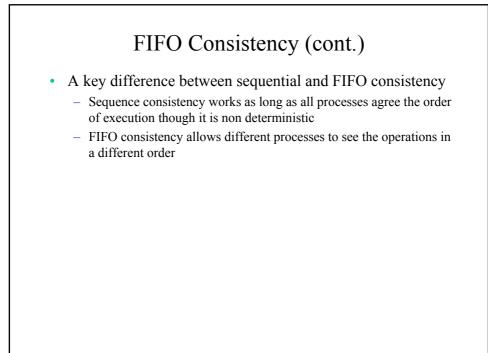


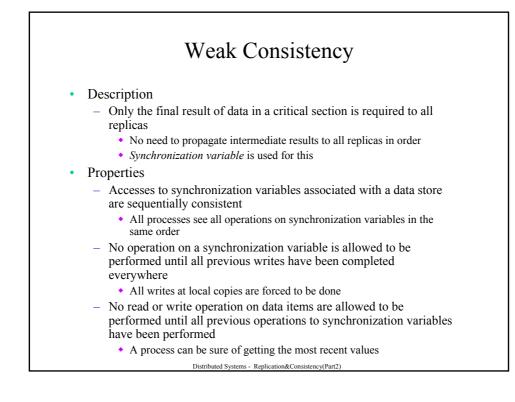
Four valid execution sequences for three concurrent processes			
Process 1 x = 1; print (y, z);	Process 2 y = 1; print (x,z);	Process 3 z = 1; print (x, y);	
x = 1;	x = 1;	y = 1;	y = 1;
print ((y, z);	y = 1;	z = 1;	x = 1;
y = 1;	print (x, z);	print (x, y);	z = 1;
print (x, z);	print (y, z);	print (x, z);	print (x, z);
z = 1;	z = 1;	x = 1;	print (y, z);
print (x, y);	print (x, y);	print (y, z);	print (x, y);
Prints: 001011	Prints: 101011	Prints: 010111	Prints: 11111
Signature:	Signature:	Signature:	Signature:
001011	101011	110101	111111

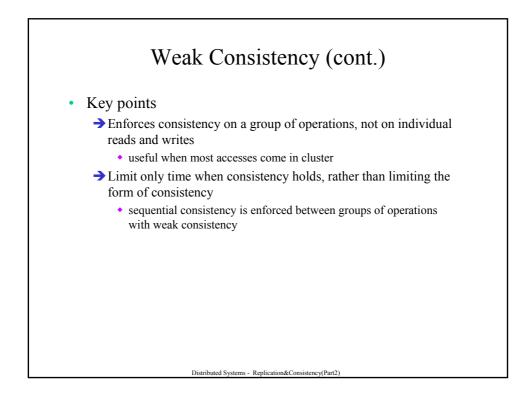


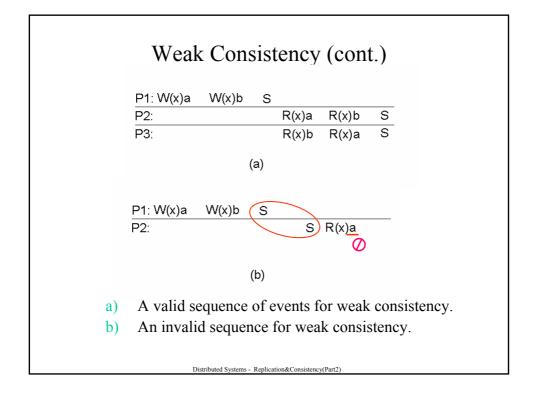


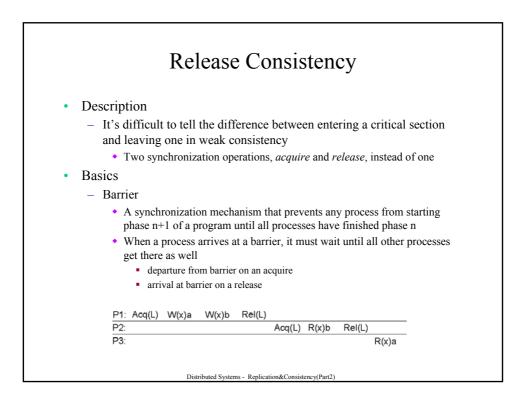


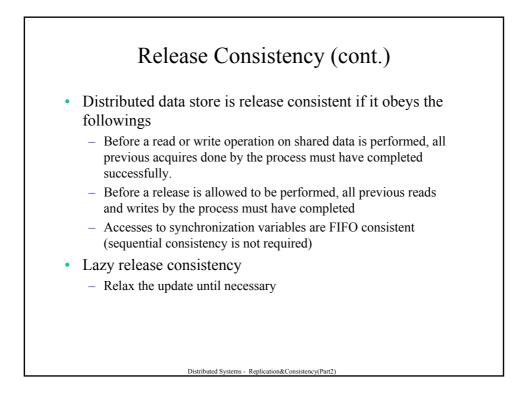


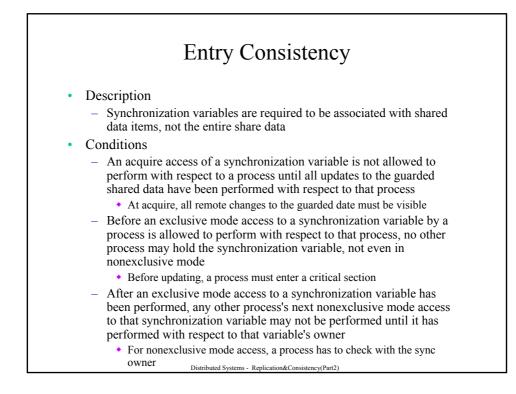












Entry Consistency (cont.)
• Example
P1: Acq(Lx) W(x)a Acq(Ly) W(y)b Rel(Lx) Rel(Ly)
P2: Acq(Lx) R(x)a R(y)ND
P3: Acq(Ly) R(y)
Distributed Systems - Replication&Consistency(Part2)

-	Description	
Strict	Absolute time ordering of all shared accesses matters.	
inearizability	All processes must see all shared accesses in the same order. Accesses are furthermore ordered according to a (non-unique) global timestamp	
Sequential	All processes see all shared accesses in the same order. Accesses are not ordered in time	
Causal	All processes see causally-related shared accesses in the same order.	
FIFO	All processes see writes from each other in the order they were used. Writes from different processes may not always be seen in that order	
	(a)	
Consistency	Description	
Weak Shared data can be counted on to be consistent only after a synchronization is		
Release	Shared data are made consistent when a critical region is exited	
Entry	Shared data pertaining to a critical region are made consistent when a critical region is entered.	
	(b)	